

1. (Twice Amended) A liquid crystal display, comprising:

Sub C1
FIG. 3. (G1H)
(DMA)
known
into 706-708b
a signal processor for generating and outputting a first image signal and a second image signal, a gray scale voltage, a gate voltage, and a driving control signal using an image data, a main control signal, and a power source all of which are supplied from an image supplying source, the driving control signal including a source driving control signal and a gate driving control signal; Col 1, line 64 - Col 2, line 12

bl
a data signal driver for generating and outputting a data signal from the first image signal and the second image signal, the gray scale voltage and the source driving control signal all of which are input from said signal processor;

a printed circuit board having a plurality of wires for transmitting the signals and/or voltages of said signal processor to said data signal driver; FIG 6, Col 2, lines 6-12.

a gate signal driver for generating and outputting a gate signal from the gate voltage and the gate driving control signal of said signal processor; and

a liquid crystal display panel for displaying an image formed by receiving the data signal from said data signal driver and the gate signal from said gate signal driver, (FIG 3, Item 100 Col 2, lines 55-64)

wherein the plurality of wires comprises a first group of wires for transmitting the first image signal and a second group of wires for transmitting the second image signal, and the first group of wires are entirely spaced apart from the second group of wires, and Col 1, line 64 - Col 2 line 12
FIG 3 Col 3, lines 7-27

wherein the data signal driver includes two groups of the data signal driver outputting a data signal from the first and the second image signal, one of which is the left-side of the signal processor and the other of which is the right-side of the processor. FIG 3, Col 3 lines 7-27

Please add the following new claims 9-12.

Sub C1
9. (Newly Added) A liquid crystal display, comprising:

a signal processor for generating and outputting a first image signal and a second image signal, a gray scale voltage, a gate voltage, and a driving control signal using an image data, a main control signal, and a power source all of which are supplied from an image supplying source, the driving control signal including a source driving control signal and a gate driving control signal;

B2
Cmt
a data signal driver for generating and outputting a data signal from the first image signal and the second image signal, the gray scale voltage and the source driving control signal all of which are input from said signal processor;

a printed circuit board having a plurality of wires for transmitting the signals and/or voltages of said signal processor to said data signal driver;

a gate signal driver for generating and outputting a gate signal from the gate voltage and the gate driving control signal of said signal processor; and

a liquid crystal display panel for displaying an image formed by receiving the data signal from said data signal driver and the gate signal from said gate signal driver,

wherein the plurality of wires comprises a first group of wires for transmitting the first image signal and a second group of wires for transmitting the second image signal, and the first group of wires are entirely spaced apart from the second group of wires, and

wherein the data signal driver comprises at least four source drive integrated circuits and is physically, electrically connected to said liquid crystal display panel by a connecting member mounting the source drive integrated circuits one to one, wherein the connecting member includes a first group of connecting member and a second group connecting

Fig 3, col 1, line 64 - col 2, line 28
col 3 lines 7-27

member, the first group of connecting member being connected with the first group of wires and the second group of connecting member being connected with the second group of wires.

10. (Newly Added) The liquid crystal display of claim 9, wherein the first image signal includes a first clock signal and the second image signal includes a second clock signal, and the first clock signal and the second clock signal have a frequency half of a clock signal frequency supplied from the image supplying source. ^(Fig 3, LCK-L) ^(Fig 3, LCK-R) ^{Paras}

B2
cancel

11. (Newly Added) The liquid crystal display of claim 9, wherein the first image signal includes a first shift signal and the second image signal includes a second shift signal, the first shift signal and the second shift signal being respectively applied to a source drive integrated circuit of a corresponding group of the source drive integrated circuits such that the group of the source drive integrated circuits have the same phase. ^{Col 4, lines 8-24}

12. (Newly Added) The liquid crystal display of claim 9, wherein the first group of wires and the second group of wires are branched from an wire aggregation including a plurality of wires at a selected position. ^{Fig 3*4}